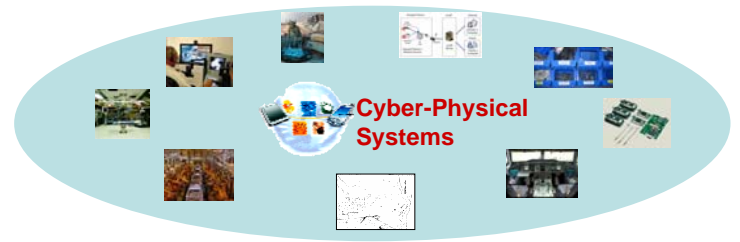


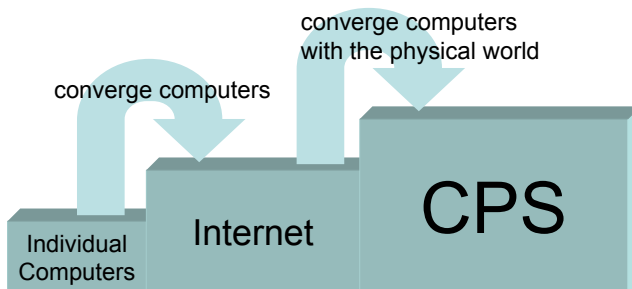
Real-Time Communications Infrastructures for Cyber-Physical Systems

Qixin Wang
Dept. of Computing
The Hong Kong Polytechnic Univ.
23/6/2009

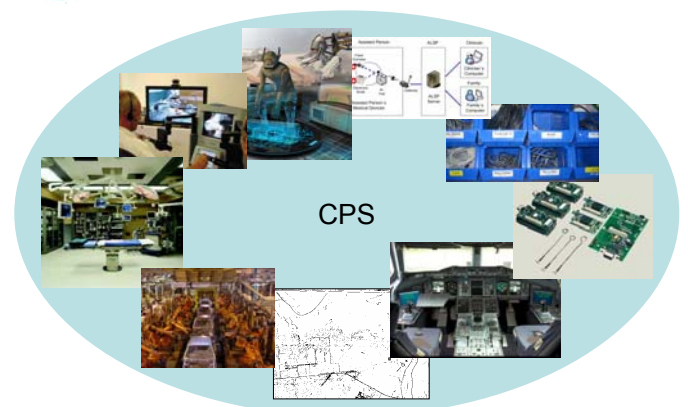


Cyber-Physical Systems (CPS) is expected to be a theme topic in CS after the Internet

Synonym to Pervasive Computing



CPS covers a large variety of applications with crucial social and economic impacts



MDPnP integrates tens of thousands of medical devices for hospitals & assisted living

Cables for connecting various monitors to anesthesia EMR



Operation Room Automation

Reduce the risk of tripping over wires

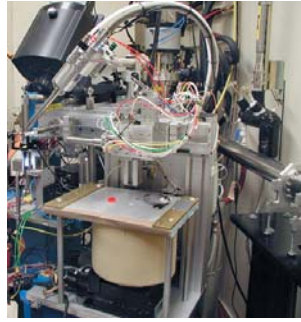


← Today
Future



Next Generation Industrial Control

More embedded systems
More interaction/complexity
Wired → Wired + Wireless



Next Generation Aircraft

Concord
1976



Next Generation Aircraft



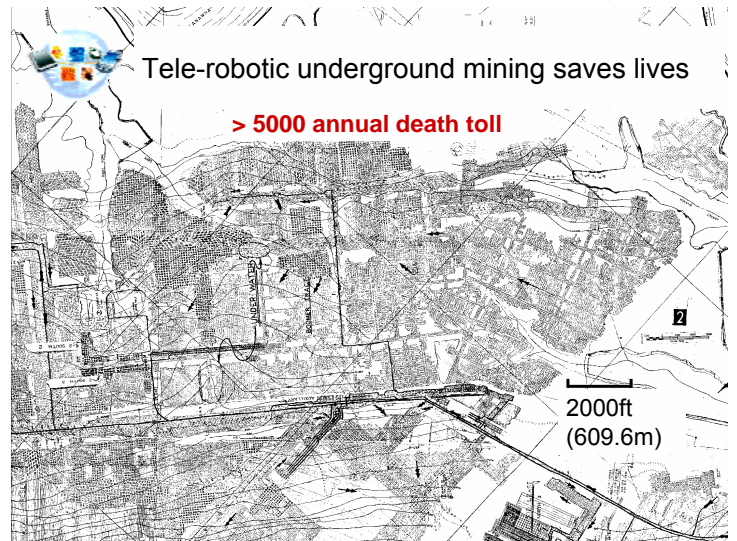
A380
2007



Telepresence lets people collaborate without moving to the same geographical location



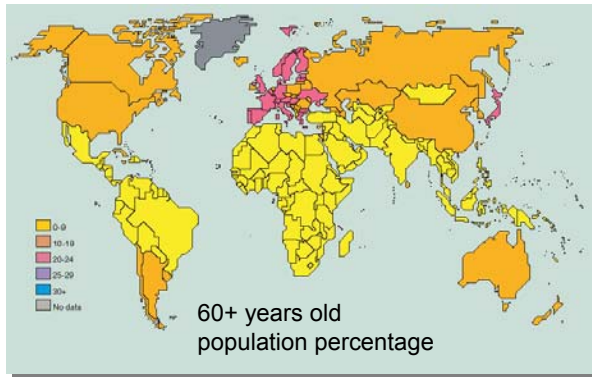
Telemedicine, Tele-Surgery save more lives





Assisted Living helps saving our economy and society from the aging crisis

2002

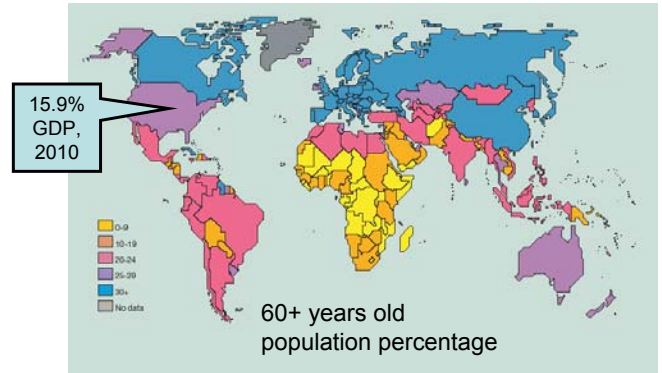


SOURCE: United Nations • "Population Aging • 2002"



Assisted Living helps saving our economy and society from the aging crisis

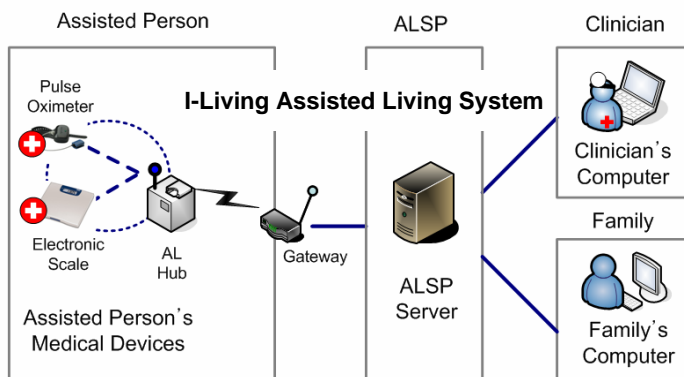
2050



SOURCE: United Nations • "Population Aging • 2002"



Assisted Living helps saving our economy and society from the aging crisis



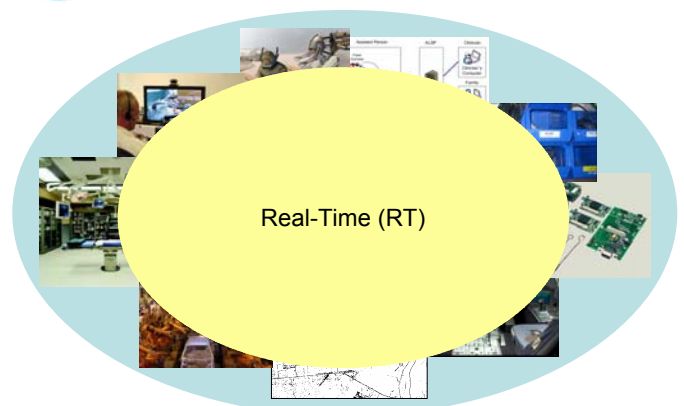
Many Other CPS Applications



Need many building block components to lay the infrastructure for CPS applications



One important category is the building blocks for real-time infrastructure





Robotic Surgery: each task is a continuous loop of sensing (or actuating) jobs

Each job:

1. Must catch deadline
2. Does not have to be fast



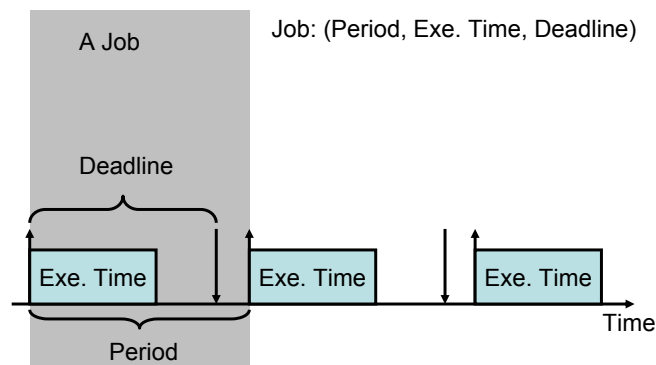
Aviation and Industrial Control: each task is a continuous loop of sensing (or actuating) jobs

Each job:

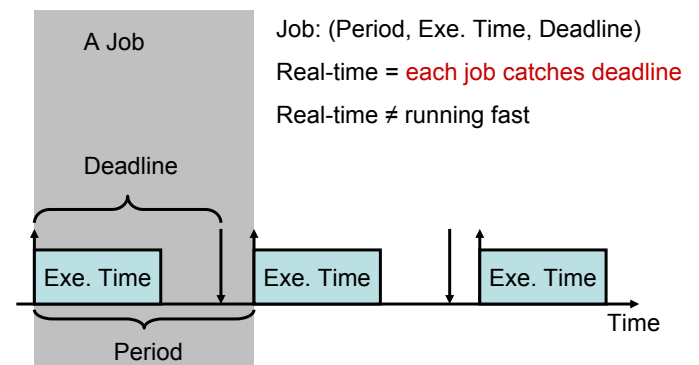
1. Must catch deadline
2. Does not have to be fast



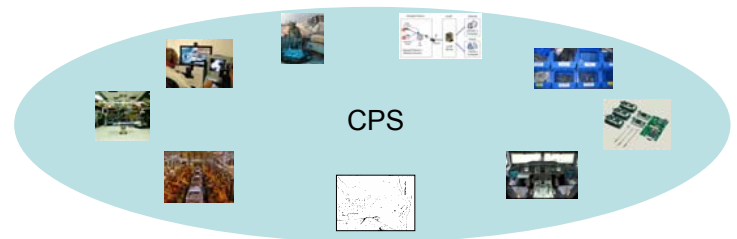
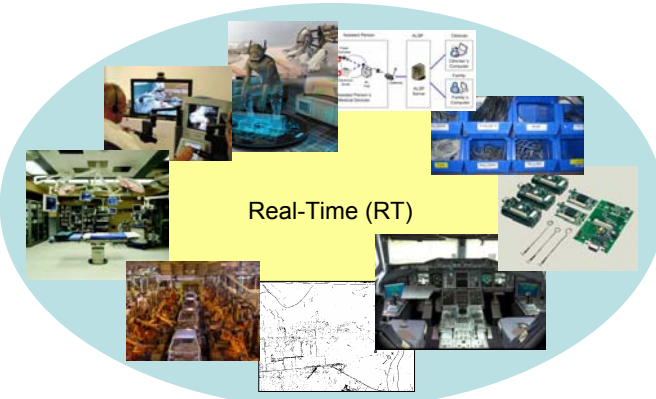
A typical real-time task is a continuous loop of periodic jobs.



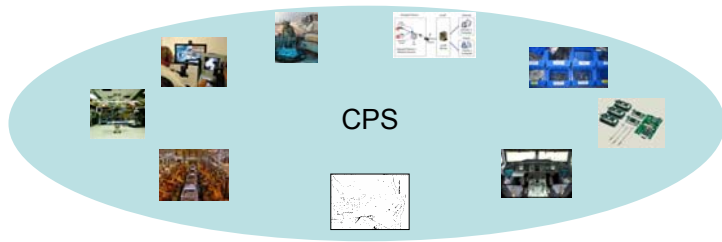
A typical real-time task is a continuous loop of periodic jobs.



CPS need real-time communication infrastructures



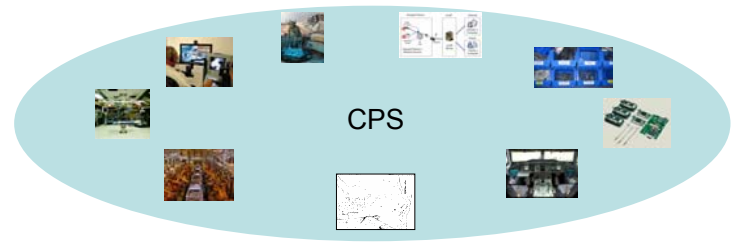
Real-Time Switch



Real-Time Switch



Real-Time Wireless LAN



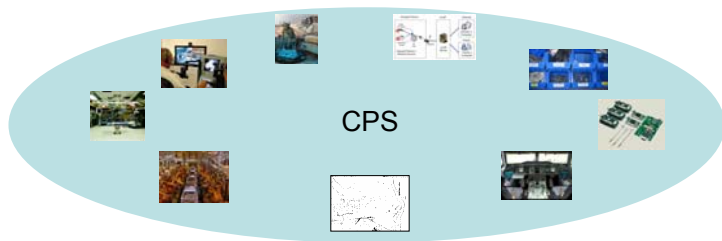
Real-Time Switch



Real-Time Wireless LAN



Real-Time Localization



Middleware



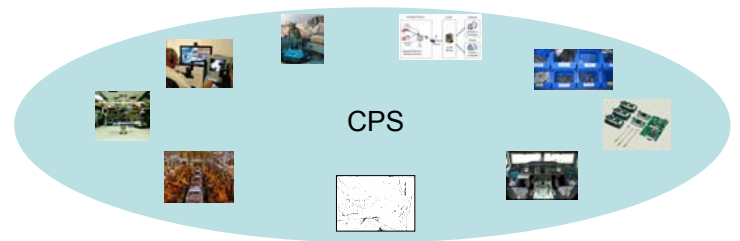
Real-Time Switch



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Real-Time Localization



Middleware



Real-Time Switch



Real-Time Wireless LAN



Real-Time Localization



Real-Time Switch is the key component for many infrastructures

Real-Time WAN, Real-Time Internet Subnet

Real-Time Fieldbus

RT High Performance Computing Architecture, e.g., InfiniBand



Real-Time Switch



Real-Time Switch is the key component for many infrastructures

Real-Time WAN, Real-Time Internet Subnet

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RT High Performance Computing
Architecture, e.g., InfiniBand

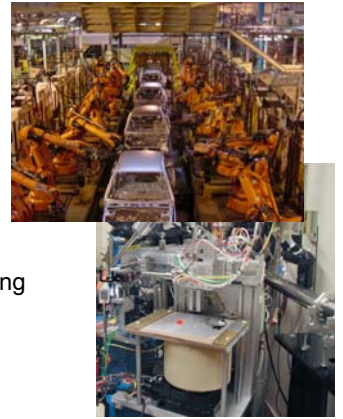


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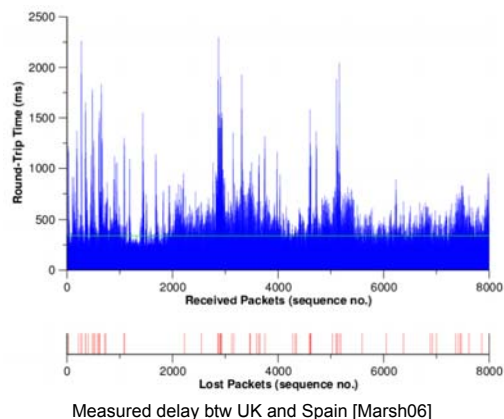


Designing real-time switch is not easy

1. A. Demers, S. Keshav, and S. Shenker, "Analysis and simulation of a fair queueing algorithm," in SIGCOMM'89, 1989, pp. 1-12.
2. D. Ferrari, and D. Verma, "A scheme for real-time channel establishment in wide-area networks," in IEEE JSAC, v.8, n3, April, 1990.
3. D. C. Verma, H. Zhang, and D. Ferrari, "Delay jitter control for real-time communication in packet switching network," in Proceedings of Tricom'91, pp. 35-46, April 1991.
4. S. Chakrabarti, "A simple statistic for congestion assessment," in IEEE INFOCOM'93, pp. 498-507, Sep. 1993.
5. L. J. 1991.
6. A. Demers, S. Keshav, and S. Shenker, "A self-clocked fair queueing algorithm: an analysis," in SIGCOMM'89, 1989, pp. 1-12.
7. A. Demers, S. Keshav, and S. Shenker, "A self-clocked fair queueing algorithm: an analysis," in SIGCOMM'89, 1989, pp. 1-12.
8. S. Jamaloddin Golestani, "A self-clocked fair queueing scheme for broadband applications," Proc. IEEE INFOCOM'94, pages 636-646, IEEE, 1994.
9. H. Zhang, "Service Disciplines For Guaranteed Performance Service in Packet-Switching Networks," Proceedings of the IEEE, 83(10), Oct 1995.
10. Jon C. R. Bennett and H. Zhang, "WF²Q: worst-case fair weighted fair queueing," IEEE INFOCOM'96, March, 1996.
11. Pawan Goyal, Harish K. Vin, and Haiqin Zhang, "Start-time fair queueing: a scheduling algorithm for integrated services packet switching networks," ACM SIGCOMM'96, pages 157-168, ACM press, August, 1996.
12. I. R. Philip, "Scheduling real-time messages in packet-switched networks," Ph.D. Thesis, Technical Report No. UIUCDS-R-96-1977, CS Dept., UIUC, Oct., 1996.
13. Subhash Suri, George Varghese, and Girish Chandramanem, "Leap forward virtual clock: A new fair queueing scheme with guaranteed delays and throughput fairness," IEEE Proc. INFOCOM'97, 1997.
14. I. Stoica, S. Shenker, and H. Zhang, "Core-stateless fair queueing: achieving approximation fair bandwidth allocations in high speed networks," Proc. of ACM SIGCOMM'98, October 1998, pp. 118-130.
15. R. K. Kulkarni, H. Kanakia, and S. Keshav, "Rate controlled servers for very high speed networks," in IEEE Global Telecommunications Conference, Dec., 1999.
16. W. Sun, and K. G. Shin, "End-to-end delay bounds for traffic aggregates under guaranteed-rate scheduling algorithms," in IEEE ToN, 13(5): 1188-1201, Oct., 2005.



Today's Internet is still NOT real-time.

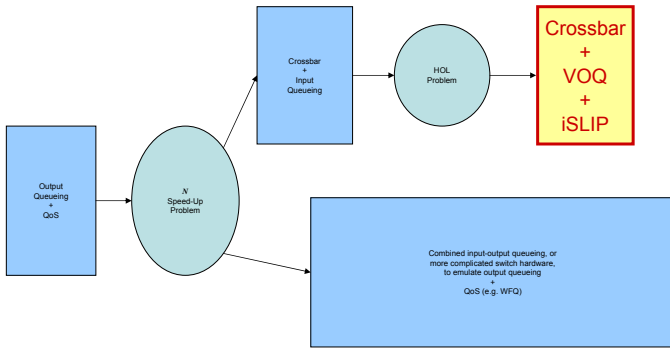


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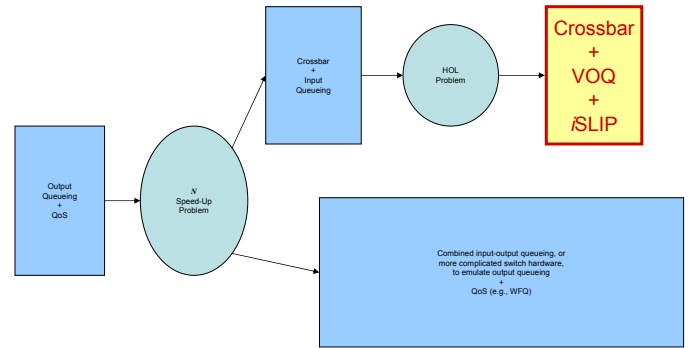
Why?



Industry acceptance has the final say. Two things rules: simplicity and switch hardware features



Past lessons tell us to design the real-time switch compatible to “crossbar + VOQ + iSLIP”



What is “Crossbar + VOQ + iSLIP”?

A widely implemented switch architecture

- Advantages
- Simple
 - High switch utilization
 - Fast adaptation to random traffic (Internet traffic)



What is “Crossbar + VOQ + iSLIP”?

Input Ports

I1

I2

I3



What is “Crossbar + VOQ + iSLIP”?

Output Ports

O1

O1

O2

O2

O3

O3



What is “Crossbar + VOQ + iSLIP”?

Virtual Output Queue (VOQ)



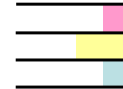
I1

O1




I2

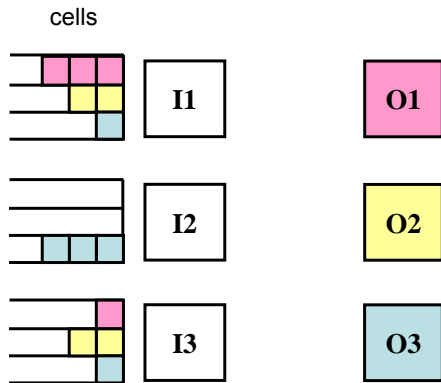
O2




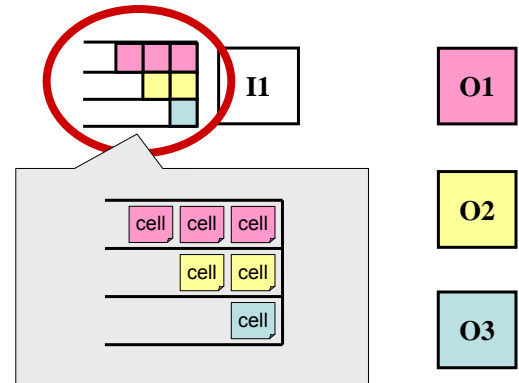
I3


O3

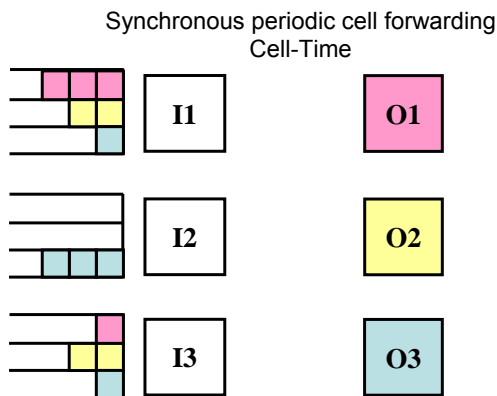
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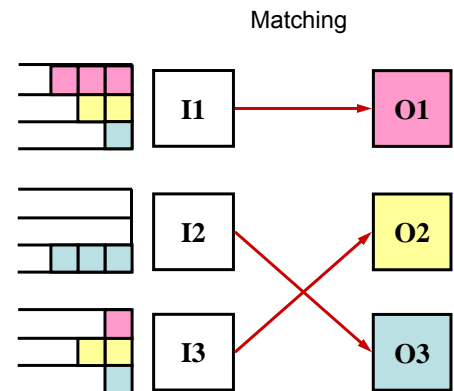



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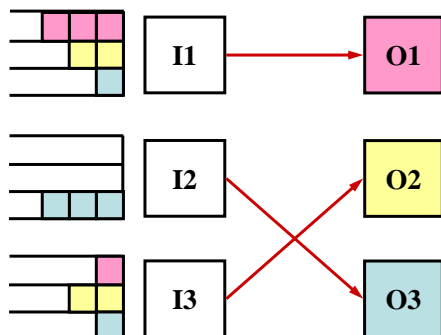
Qixin Wang, UIUC, 6/24/2009


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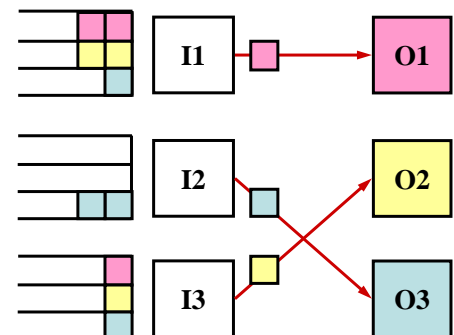



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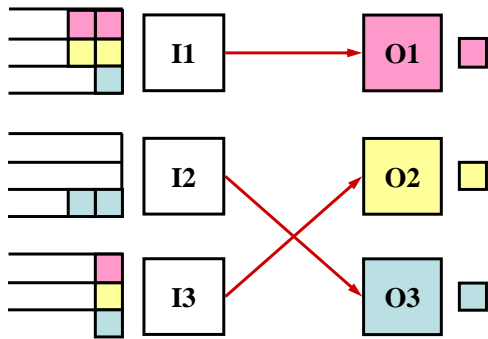
Why Matching? An input/output can only send/receive one cell per cell-time




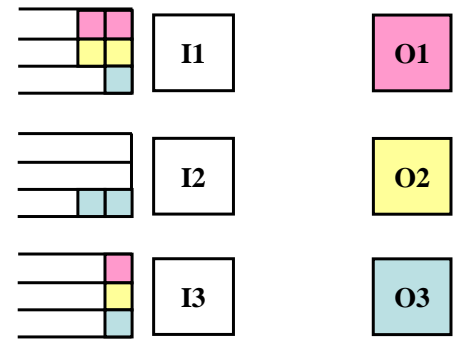
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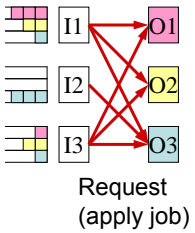
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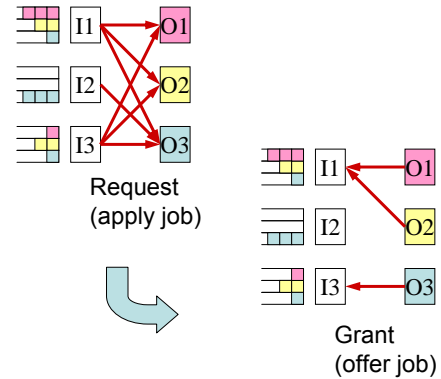
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


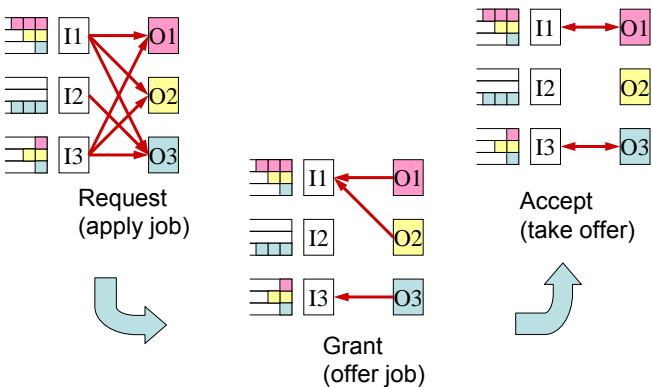
 What is “Crossbar + VOQ + *i*SLIP”?
iSLIP is like faculty job hunting ☺




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iSLIP is like faculty job hunting ☺



 But we cannot directly adopt “crossbar + VOQ + iSLIP”

Huge per hop delay bound because of poor isolation;

E2E delay bound is an open problem [Gopalakrishnan06].



But we cannot directly adopt “crossbar + VOQ + iSLIP”

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What to do?



But we cannot directly adopt “crossbar + VOQ + iSLIP”

Huge per hop delay bound because of poor isolation;

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What to do?

Look at **changed assumptions** and **new features**.



Changed assumption: traffic predictability

iSLIP is for non-real-time traffic: Unpredictable



Changed assumption: traffic predictability

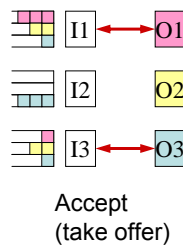
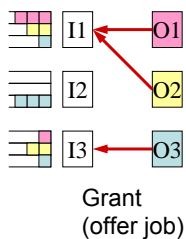
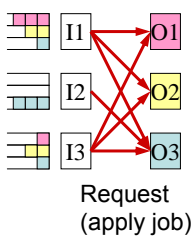
iSLIP is for non-real-time traffic: Unpredictable

Flows change rapidly

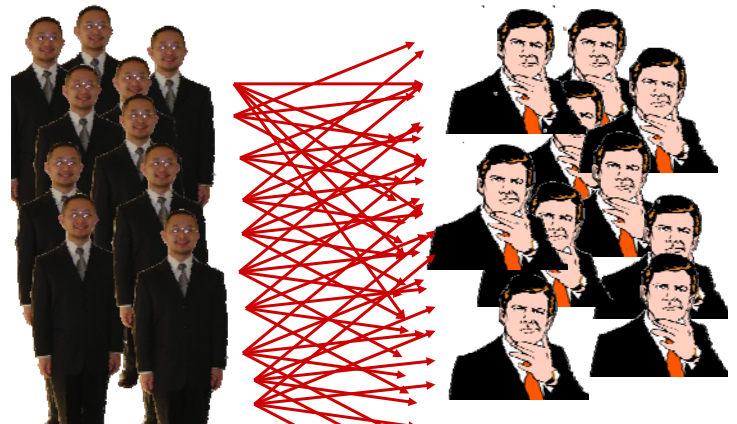
Unpredictable message size and arrival



Unpredictable traffic forces iSLIP to negotiate for each cell-time



Just like faculty job application, iSLIP's negotiate is dynamic, hard to give tight delay upper bound.





Changed assumption: traffic predictability

SLIP is for non-real-time traffic: Unpredictable
Flows change rapidly
Unpredictable message size and arrival

RT-Switch for real-time traffic: Deterministic



Changed assumption: traffic predictability

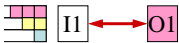
SLIP is for non-real-time traffic: Unpredictable
Flows change rapidly
Unpredictable message size and arrival

RT-Switch for real-time traffic: Deterministic
Flows rarely change
Regular message size and arrival
Non-real-time traffic → real-time traffic

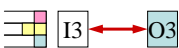
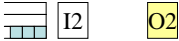


Deterministic traffic allows RT-Switch to forward cells like clockwork, no need to negotiate!

Deterministic Grant is enough



No need for Request and Accept



Simplifies instead of extends SLIP



New feature: Large message arrival period allows clock-driven time slicing

Real-time traffic



New feature: Large message arrival period allows clock-driven time slicing

Real-time traffic
Small msg size (sensing: 1cell/msg, TV quality video: 480cell/msg)



New feature: Large message arrival period allows clock-driven time slicing

Real-time traffic
Small msg size (sensing: 1cell/msg, TV quality video: 480cell/msg)
Short per msg transmission time (s: 0.5us, v: 240us)



New feature: Large message arrival period allows clock-driven time slicing

Real-time traffic

- Small msg size (sensing: 1cell/msg, TV quality video: 480cell/msg)
- Short per msg transmission time (s: 0.5us, v: 240us)
- Large msg arrival period (s: 10ms, v: 30ms)



New feature: Large message arrival period allows clock-driven time slicing

Real-time traffic

- Small msg size (sensing: 1cell/msg, TV quality video: 480cell/msg)
- Short per msg transmission time (s: 0.5us, v: 240us)
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Reminds us of clock-driven time slicing OS



New feature: Large message arrival period allows clock-driven time slicing

Real-time traffic

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Reminds us of clock-driven time slicing OS

RT-Switch runs fine time grain clock period (1ms), serving time slices (cell-time) to coarse time grain periodic RT flows



Solution: crossbar RT-Switch runs deterministic clock-driven time slicing



Solution: crossbar RT-Switch runs deterministic clock-driven time slicing

Clock period of M cell-time, e.g., $M = 5$

Cell time:	1	2	3	4	5
I1:	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
I2:	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
I3:	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
I4:	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>



Solution: crossbar RT-Switch runs deterministic clock-driven time slicing

Clock period of M cell-time, e.g., $M = 5$

Fit original real-time flow-forwarding tasks into clock period, e.g., (11, 3) \rightarrow (5, 2), i.e., (10, 4)

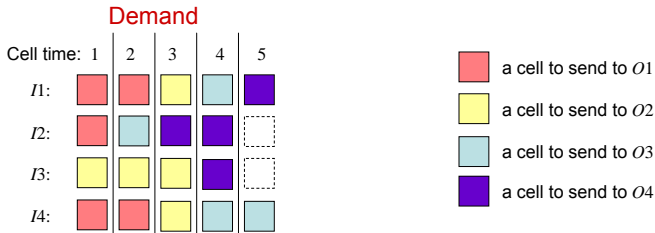
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I3:	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
I4:	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>



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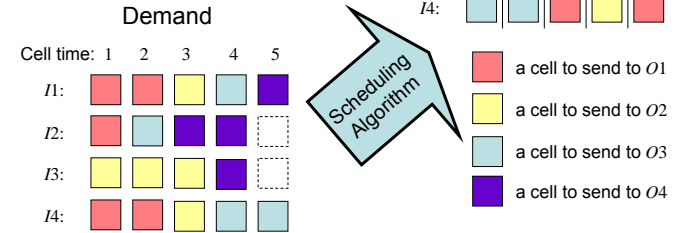


Solution: crossbar RT-Switch runs deterministic clock-driven time slicing

Clock period of M cell-time

Fit original RT tasks into clk period

Config. time scheduling ($O(N^4)$)



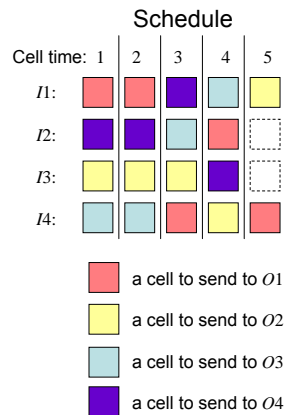
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Config. time scheduling ($O(N^4)$)

Forward cells according to the schedule during runtime



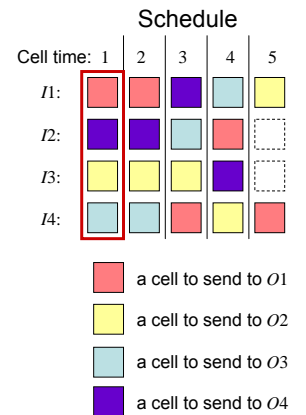
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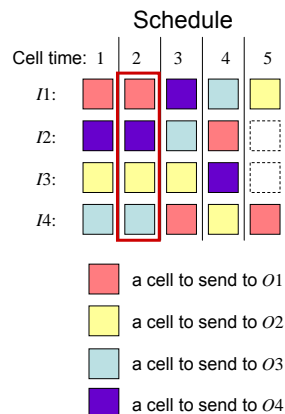
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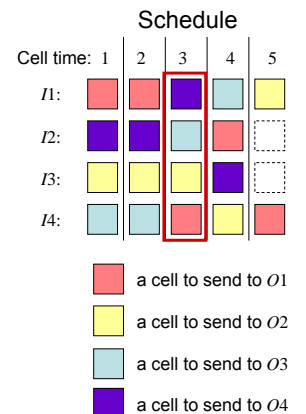
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Clock period of M cell-time

Fit original RT tasks into clk period

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Forward cells according to the schedule during runtime

	Schedule				
	Cell time: 1	2	3	4	5
$I1:$					
$I2:$					
$I3:$					
$I4:$					

a cell to send to $O1$
 a cell to send to $O2$
 a cell to send to $O3$
 a cell to send to $O4$



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 a cell to send to $O3$
 a cell to send to $O4$



Solution: crossbar RT-Switch runs deterministic clock-driven time slicing

Satisfies demands:

- Real-Time Guarantee
- Backward Compatibility
- Simplicity
- Performance
- Isolation

	Schedule				
	Cell time: 1	2	3	4	5
$I1:$					
$I2:$					
$I3:$					
$I4:$					

a cell to send to $O1$
 a cell to send to $O2$
 a cell to send to $O3$
 a cell to send to $O4$



Evaluation: Schedulability

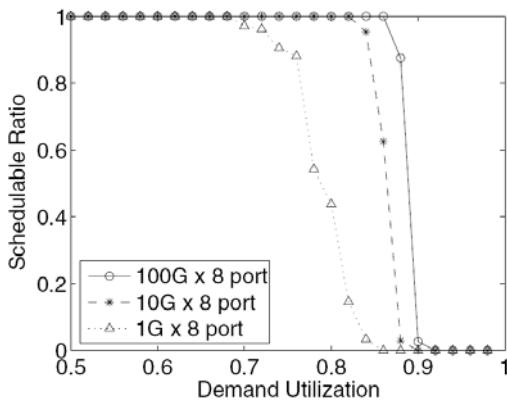
Randomized traffic demand based on typical real-time industrial/medical application models

Total demanded switch utilization is U

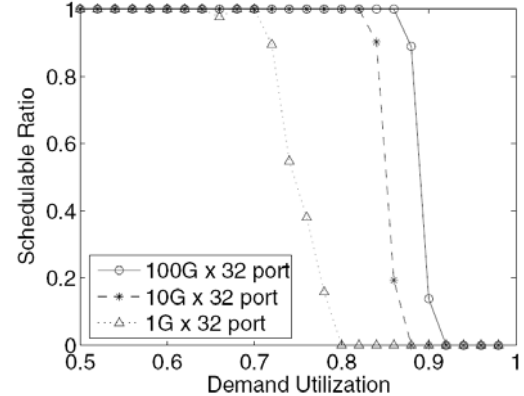
Given U , what is the schedulable ratio?



Evaluation: Schedulability



Evaluation: Schedulability





Evaluation: E2E Delay Bound

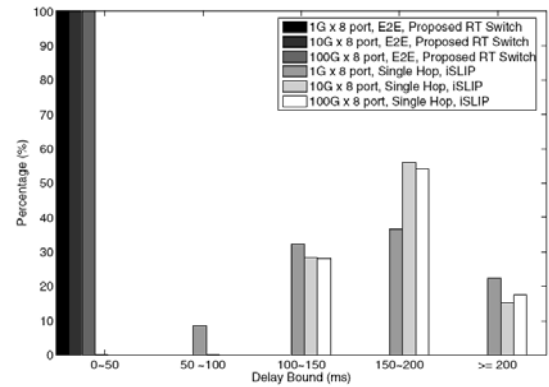
Randomized traffic demand based on typical real-time industrial/medical application models

Max hop count is 15

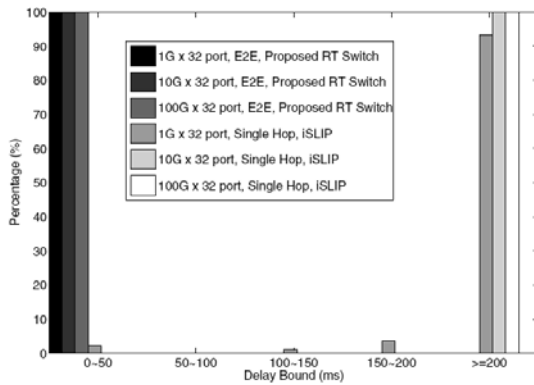
Compare E2E Delay Bound provided by Real-Time Switch and iSLIP



Evaluation: E2E Delay Bound



Evaluation: E2E Delay Bound



Conclusion

Short E2E delay guarantee

Good schedulability

Compatible to, and simpler than the widely implemented iSLIP crossbar switch.

$O(1)$ runtime computation

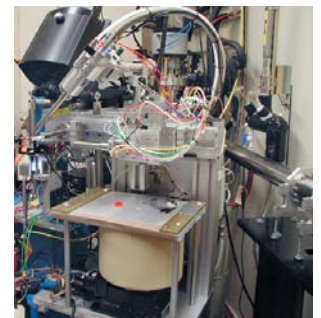
Polynomial configuration time computation




The demand for real-time wireless communication is increasing.

Mechanical Freedom / Mobility

Ease of Deployment / Flexibility




**Real-Time
Wireless LAN**

 The demand for real-time wireless communication is increasing.

Cables for connecting various monitors to anesthesia EMR




 The demand for real-time wireless communication is increasing.

Reduce the risk of tripping over wires




Future

← Today

 **Reliability and Robustness** is the top concern for real-time wireless communication.

Cannot back off under adverse wireless channel conditions

 **Reliability and Robustness** is the top concern for real-time wireless communication.

Cannot back off under adverse wireless channel conditions




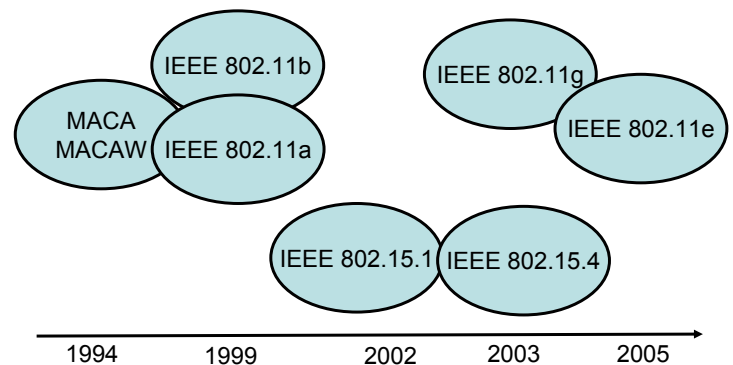
 **Reliability and Robustness** is the top concern for real-time wireless communication.

Adverse wireless medium

- Large scale path-loss
- Multipath
- Persistent electric-magnetic interference
- Same-band / adjacent-band RF devices



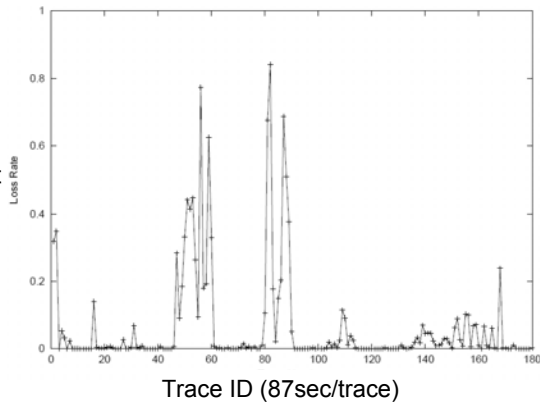
 Nowadays wireless LANs are NOT real-time.





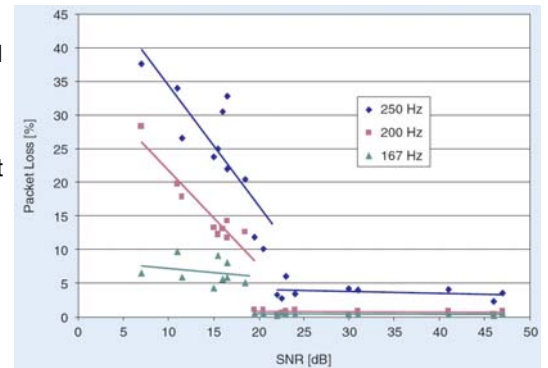
Nowadays wireless LANs are NOT real-time.

IEEE 802.11
packet loss
rate in an
industrial
environment
[Willig02]



Nowadays wireless LANs are NOT real-time.

IEEE 802.11
packet loss
rate in an
office
environment
[Ploplys04]



Nowadays wireless LANs are NOT real-time.

Why?

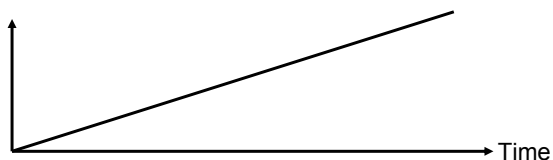


Design philosophy mismatch: pursuing large data throughput & short delay



Design philosophy mismatch: pursuing large data throughput & short delay

Signal
Energy

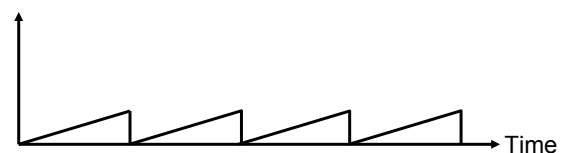



Design philosophy mismatch: pursuing large data throughput & short delay

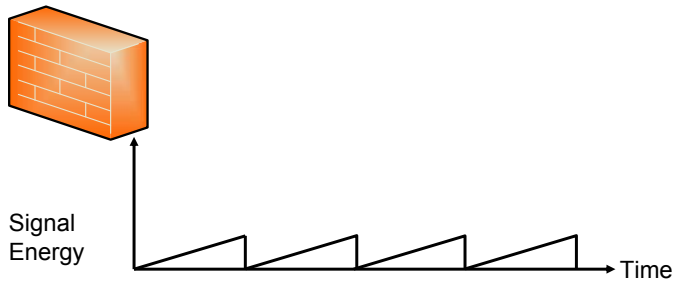
Send packet fast


Do not spend much time accumulate strength

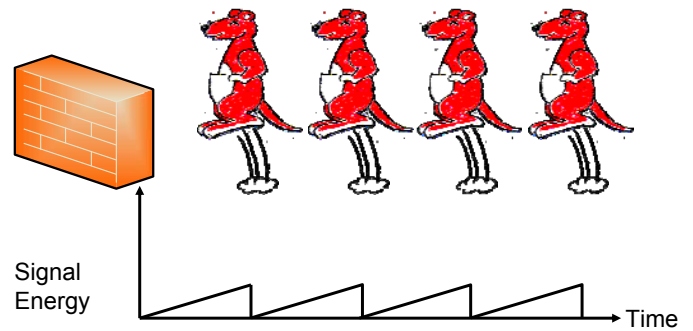
Signal
Energy




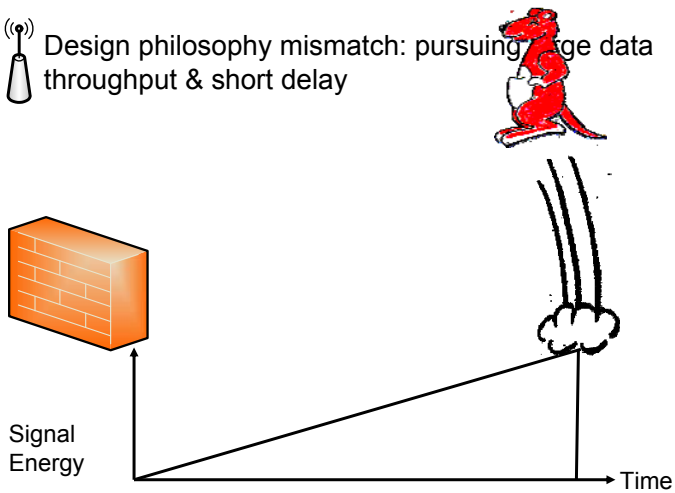
 Design philosophy mismatch: pursuing large data throughput & short delay




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 Observation: Real-time communications are usually persistent connections with low data rate

Typical inter-node traffic:
100~200 bit/pkt, 10~1 pkt/sec per connection.

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100~200 bit/pkt, 10~1 pkt/sec per connection.

Information Theory:
Lower data rate → higher robustness.

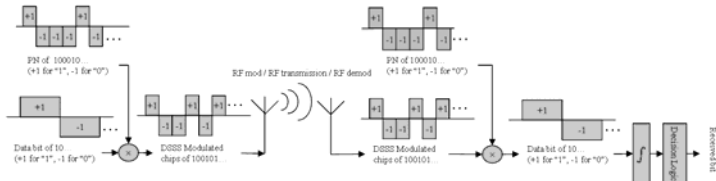
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Typical inter-node traffic:
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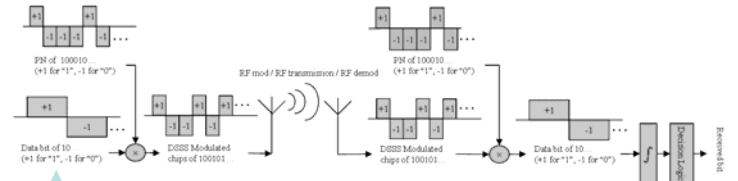
Information Theory:
Lower data rate → higher robustness.

Direct Sequence Spread Spectrum (DSSS) Technology:
Lower data rate ↔ Higher robustness

Tutorial on DSSS



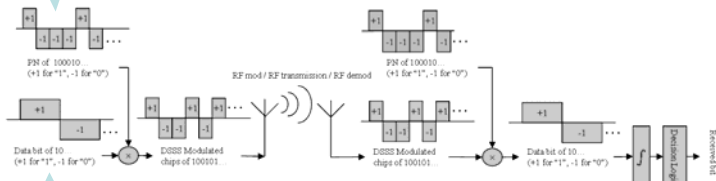
Tutorial on DSSS



Data stream,
a.k.a bit
stream. Bit
rate: r_b .

Tutorial on DSSS

Pseudo Noise
Sequence
(PN) Stream,
a.k.a chip
stream. Chip
rate: R_c .

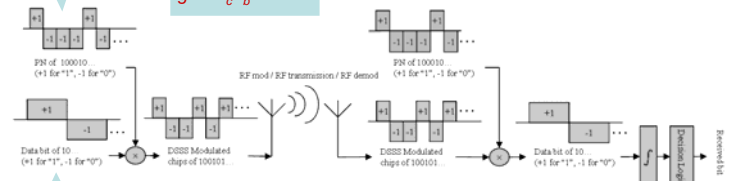


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Definition:
Processing Gain
 $g := R_c/r_b$.

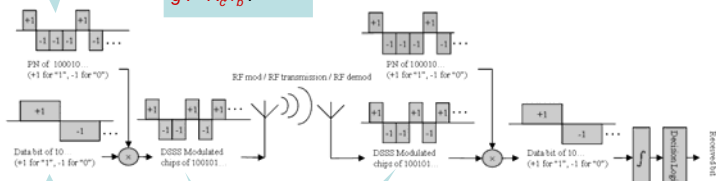


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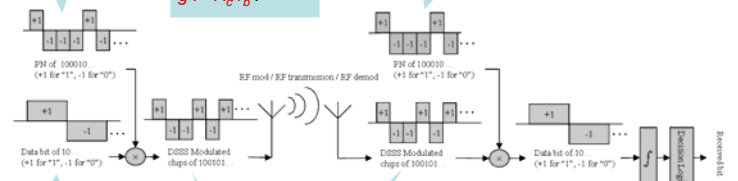
DSSS
Modulated
Stream, a.k.a
Scrambled
Stream

Tutorial on DSSS

Pseudo Noise
Sequence
(PN) Stream,
a.k.a chip
stream. Chip
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 $g := R_c/r_b$.

Same PN
Sequence



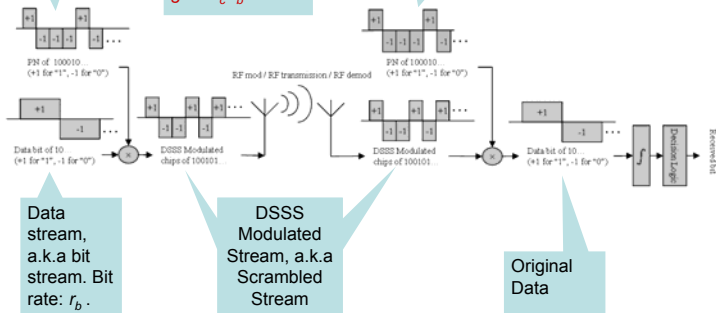
Data stream,
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DSSS
Modulated
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Pseudo Noise Sequence (PN) Stream, a.k.a chip stream. Chip rate: R_c .

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Same PN Sequence

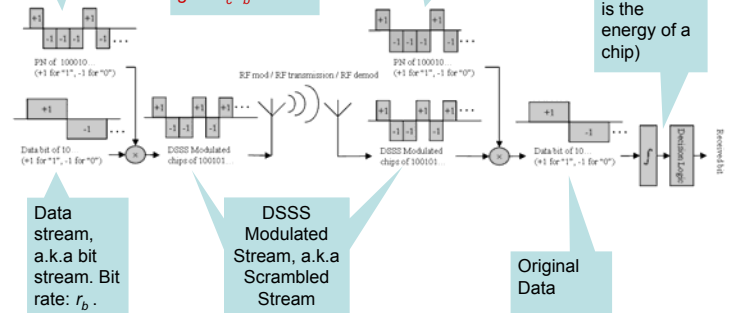


Tutorial on DSSS

Pseudo Noise Sequence (PN) Stream, a.k.a chip stream. Chip rate: R_c .

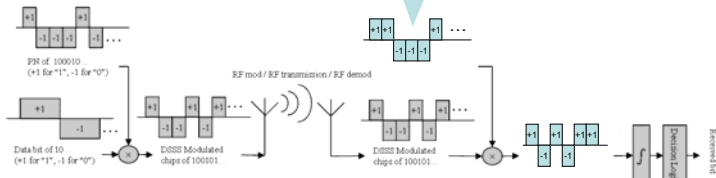
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Same PN Sequence



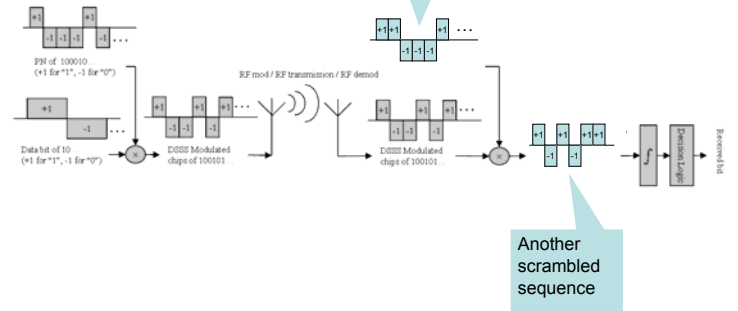
Tutorial on DSSS

If a different PN Sequence is applied



Tutorial on DSSS

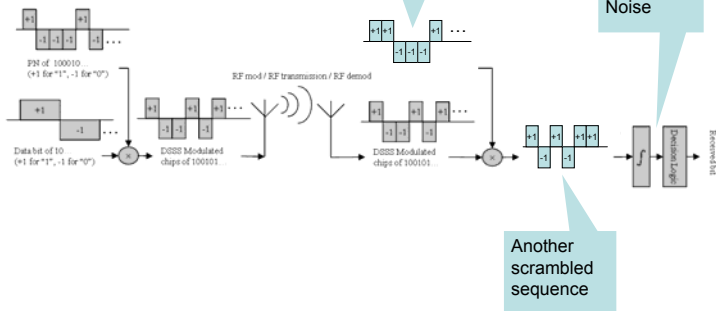
If a different PN Sequence is applied



Tutorial on DSSS

If a different PN Sequence is applied

Integration = Gaussian Noise



Observation


Bit Error Rate

Processing Gain

$$P_{ber} \leq \exp \left(- \frac{gP_u}{J + \sum_{i=1, i \neq u}^{\Xi} P_i + \sum_{h=1}^H A_h + P_u} \right)$$


- DSSS Technology:

Larger Processing Gain $g \Leftrightarrow$ Lower data rate \Leftrightarrow Lower Bit Error Rate (Higher robustness)

 Observation: DSSS can exploit low data rate to achieve higher robustness

$$P_{BER} \leq \exp(-gK) = \exp\left(-\frac{K}{r_b}\right)$$


DSSS BER Upper Bound

 Observation: DSSS can exploit low data rate to achieve higher robustness

Bit Error Rate

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Constant

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Bit Rate

DSSS BER Upper Bound
Lower data rate r_b

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Bit Error Rate

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Constant

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Constant

Bit Rate

DSSS BER Upper Bound


Lower data rate $r_b \leftrightarrow$ Larger Processing Gain $g \leftrightarrow$
Lower Bit Error Rate P_{BER} (higher robustness)

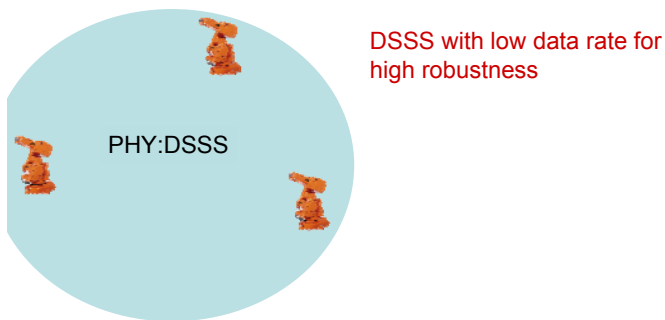
 Key Idea: How to configure for max robustness for adverse wireless medium?


 Key Idea: How to configure for max robustness for adverse wireless medium?

Answer: Use DSSS, deploy as slow data rate r_b (i.e., as large processing gain g) as the application allows.

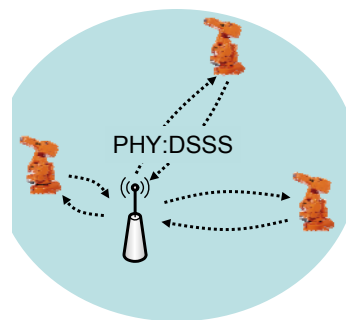
$$P_{BER} \leq \exp(-gK) = \exp\left(-\frac{K}{r_b}\right)$$


 Solution Heuristics



 Observation: Centralized, last-hop wireless scheme is preferred

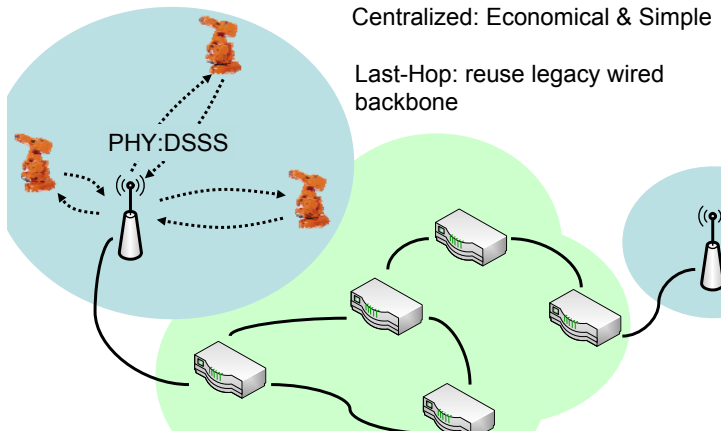
Centralized: Economical & Simple




 Observation: Centralized, last-hop wireless scheme is preferred

Centralized: Economical & Simple

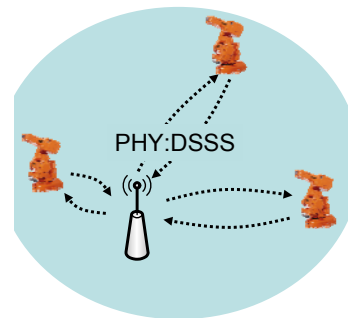
Last-Hop: reuse legacy wired backbone




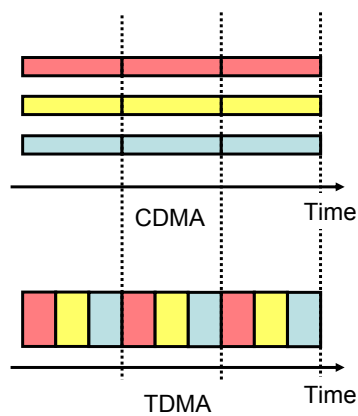
 Solution Heuristics


DSSS with low data rate for high robustness

Centralized WLAN paradigm

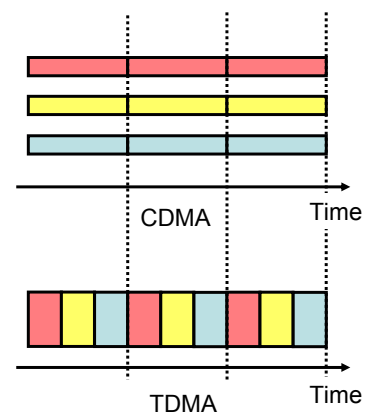



 Observation: CDMA is better than TDMA (e.g., IEEE 802.11 PCF).



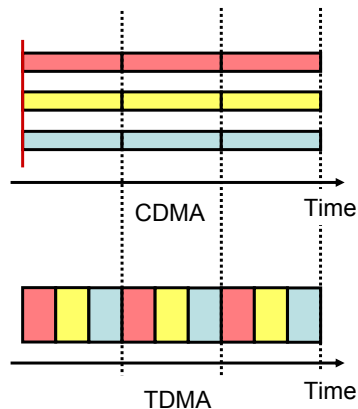
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Smaller overhead under adverse channel conditions



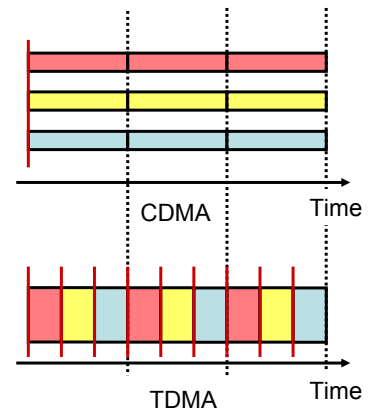
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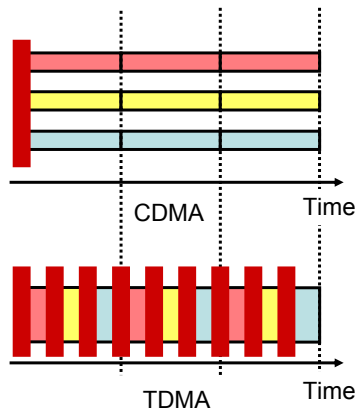
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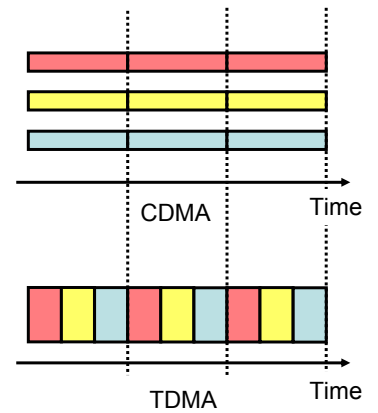
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Smaller overhead under adverse channel conditions

Easier to schedule

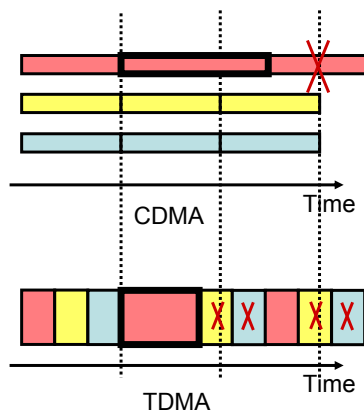



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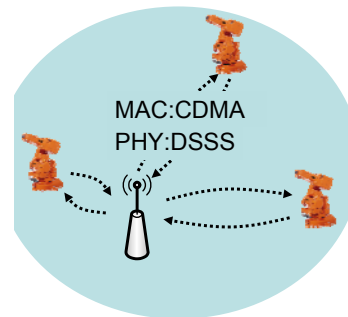
Smaller overhead under adverse channel conditions

Easier to schedule

Better overrun isolation




 Solution Heuristics

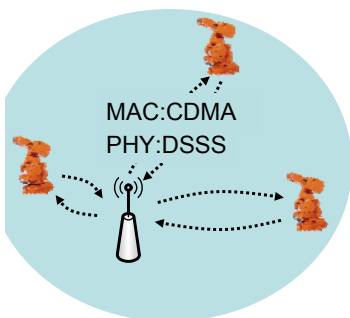


Centralized WLAN paradigm

DSSS with low data rate for high robustness

CDMA instead of TDMA


 Solution Heuristics → Choose DSSS-CDMA cell phone network paradigm!



DSSS with low data rate for high robustness

Centralized WLAN paradigm

CDMA instead of TDMA

 Simulation and Comparisons


Wireless medium model complies with typical settings for industrial environments [Rappaport02]:

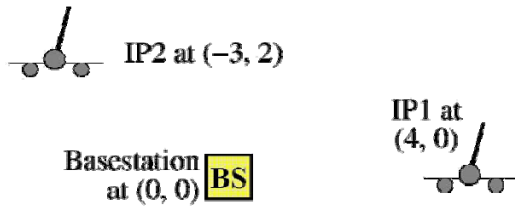
Table 1. Wireless Medium Model


Large-scale path loss	Log-normal shadowing model with $\beta = 4 \sim 6$, $\sigma = 6.8\text{dB}$ *
Small-scale fading model	Rayleigh
Multipath max excess delay	90.909nsec
Additive White Gaussian Noise [†]	Spectral density = -174dBm/Hz

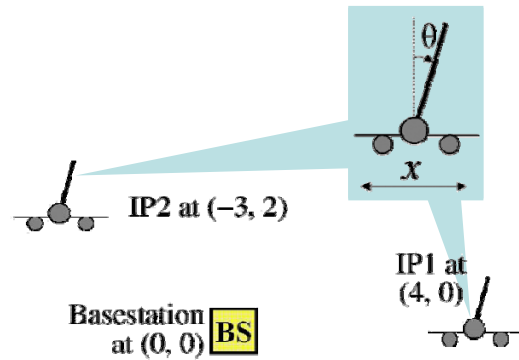
* β is the path loss exponent, σ is the log-normal standard deviation.


[†] Typically refers to thermal noise.

 A simulated demo showing DSSS-CDMA tolerates RF jamming, while IEEE 802.11b cannot

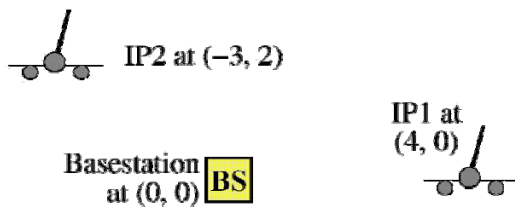



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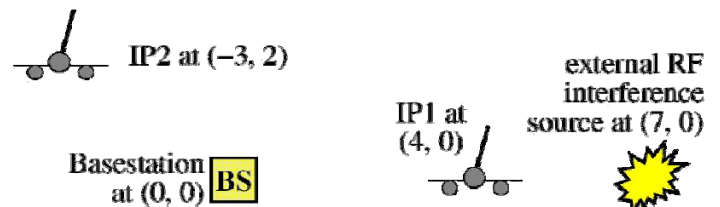
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Typical industrial environment wireless medium model



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Typical industrial environment wireless medium model

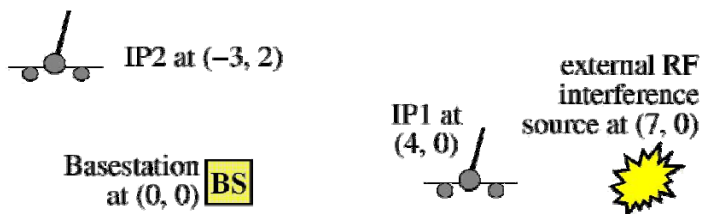



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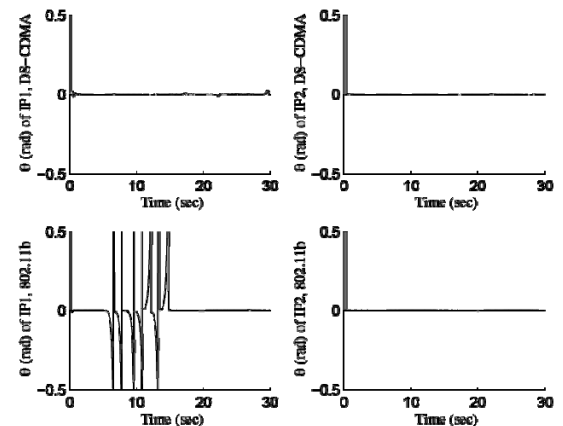
Comparison:

DSSS-CDMA: lowest data rate

IEEE 802.11b: keep retransmitting



 A simulated demo showing DSSS-CDMA tolerates RF jamming, while IEEE 802.11b cannot





A Monte-Carlo simulation showing DSSS-CDMA is more robust than IEEE 802.11a/b

Monte-Carlo simulation setup

20m x 20m room, base station at the center

n ($n = 1, \dots, 100$) remote stations, random layout

200 trials for each n

Typical industrial environment wireless medium model

Robustness Method:

DSSS-CDMA: lowest data rate

IEEE 802.11a/b: keep retransmitting



A Monte-Carlo simulation showing DSSS-CDMA is more robust than IEEE 802.11a/b

802.11:

- Use the most robust mode:
 - 802.11b (DSSS): 1, 2, 5.5, 11Mbps
 - 802.11a (OFDM): 6, 9, 12, 18, 24, 36, 48, 54Mbps
- Under adverse channel conditions, 802.11 keeps retransmitting (PCF).

DSSS-CDMA

- Deploy as slow data rate as (i.e., as large processing gain g as) the application allows (proposition 1).
- Keep transmitting even under adverse channel conditions.



Simulation and Comparisons

Table 2. Physical Layer Settings for Comparisons

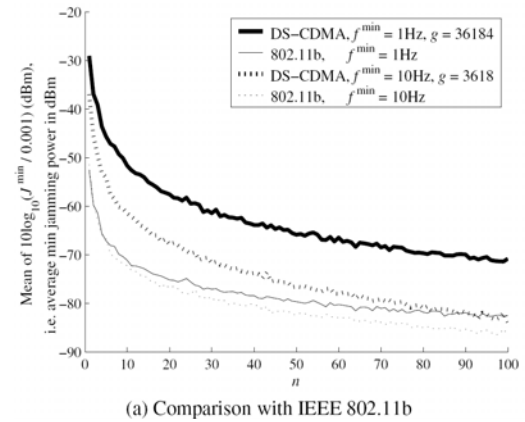
	Max trans power*	RF band†
DSSS-CDMA vs. IEEE 802.11b comparison	1 watt	2.425 ~ 2.449GHz
DSSS-CDMA vs. IEEE 802.11a comparison	800mw	5.735 ~ 5.795GHz

* According to FCC regulation.

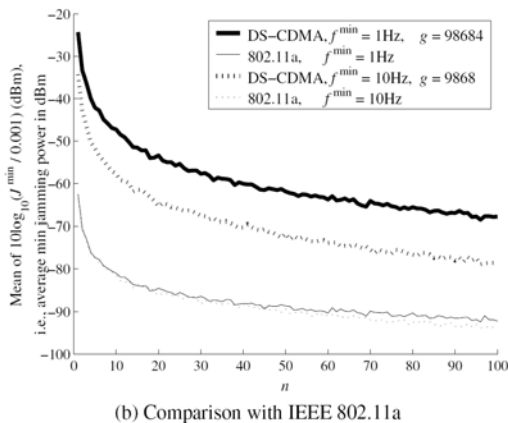
† According to IEEE 802.11 specification. Note RF bandwidth also decides baseband bandwidth (i.e. chip rate for DSSS and bit rate for OFDM).



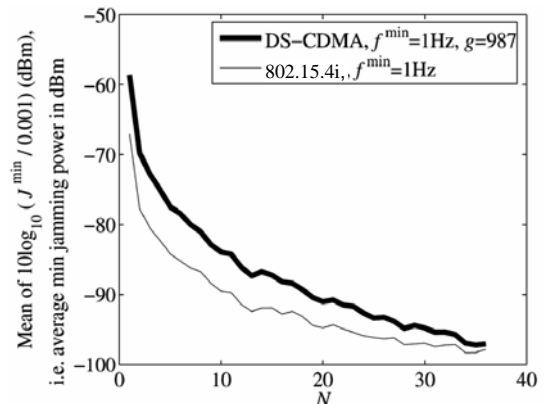
A Monte-Carlo simulation showing DSSS-CDMA is more robust than IEEE 802.11a/b



A Monte-Carlo simulation showing DSSS-CDMA is more robust than IEEE 802.11a/b

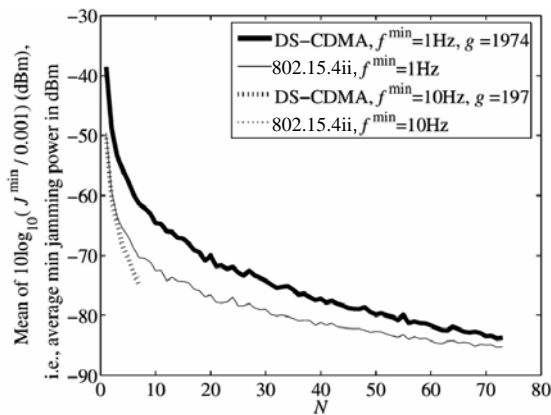


Monte Carlo comparison with IEEE 802.15.4

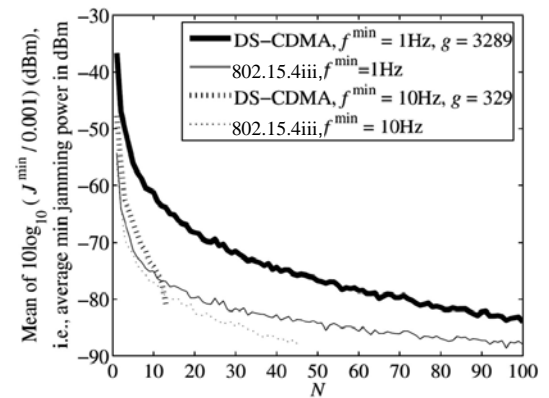




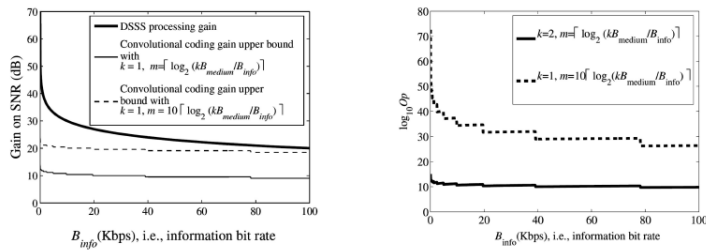
Monte Carlo comparison with IEEE 802.15.4



Monte Carlo comparison with IEEE 802.15.4



Feasibility of Convolutional Coding



k input bits, m shift registers



Conclusion

“DSSS-CDMA Cell Phone Paradigm + Slowest Data Rate”
is more robust than “IEEE 802.11 + Retransmission”.



Conclusion

“DSSS-CDMA Cell Phone Paradigm + Slowest Data Rate”
is more robust than “IEEE 802.11 + Retransmission”.

For real-time wireless LAN, change philosophy from
pursuing throughput/delay to pursuing reliability/robustness.



Future Work and Vision



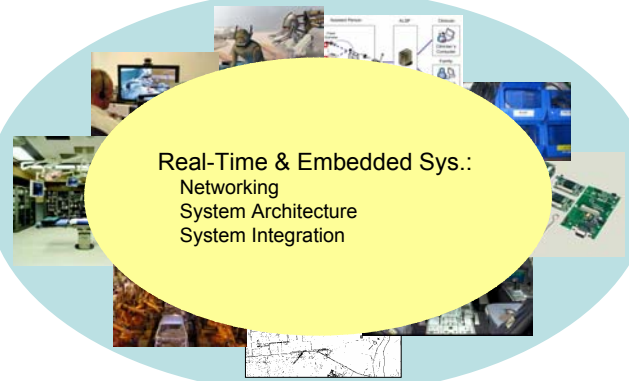
CPS covers a large variety of applications with crucial social and economic impacts



CPS



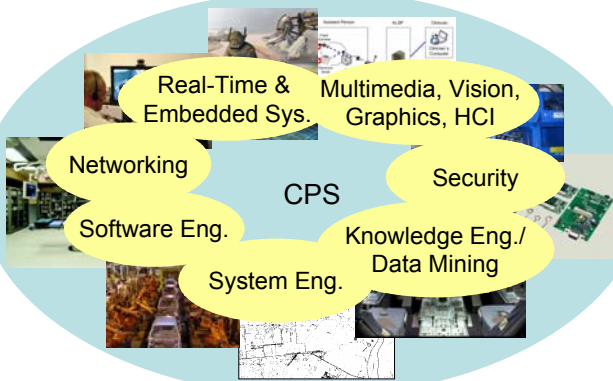
Real-Time and Embedded Systems Infrastructure is one of the CPS challenges



Real-Time & Embedded Sys.:
Networking
System Architecture
System Integration



But Real-Time is far from all; CPS' challenges involve nearly every aspects of CS.

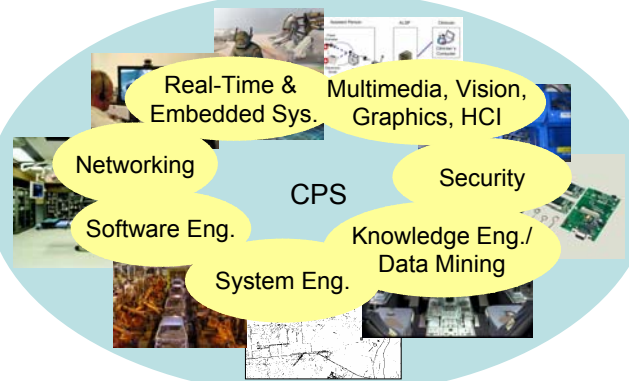


Real-Time & Embedded Sys.
Multimedia, Vision, Graphics, HCI
Networking
Security
Software Eng.
Knowledge Eng./ Data Mining
System Eng.

CPS



CPS and Pervasive Computing will bring Computer Science a promising future.



Real-Time & Embedded Sys.
Multimedia, Vision, Graphics, HCI
Networking
Security
Software Eng.
Knowledge Eng./ Data Mining
System Eng.

CPS

Thank You!

Publications

- Journal Publications:
- [TWC] Qixin Wang, Rong Zheng, Ajay Tirumala, Xue Liu, and Lui Sha, "Lightning: A Hard Real-Time, Fast, and Lightweight Low-End Wireless Sensor Election Protocol for Acoustic Event Localization", (accepted for publication) in IEEE Transactions on Mobile Computing.
 - [TMC07] Qixin Wang, Xue Liu, Weiqun Chen, Marco Caccamo, and Lui Sha, "Building Robust Wireless LAN for Industrial Control with the DSSS-CDMA Cell Phone Network Paradigm", in IEEE Transactions on Mobile Computing, vol. 6, number 6, June, 2007.
 - [TOSN06] Xue Liu, Qixin Wang, Wenbo He, Marco Caccamo, and Lui Sha, "Optimal Real-Time Sampling Rate Assignment for Wireless Sensor Networks", in ACM Transactions on Sensor Networks, vol. 2, issue 2, May, 2006.
- Conference, Workshop and Other Publications:
- [RTAS08] Qixin Wang, Sathish Gopalakrishnan, Xue Liu, and Lui Sha, "A Switch Design for Real-Time Industrial Networks", (full paper accepted for publication) in Proceedings of the 14th IEEE Real-Time and Embedded Technology and Applications Symposium (RTAS 2008), 2008.
 - [RTSS07] Qixin Wang, Xue Liu, Jennifer Hou, and Lui Sha, "QD-Aggregate: A WAN Virtual Topology Building Tool for Hard Real-Time and Embedded Applications", in Proceedings of the 28th IEEE Real-Time Systems Symposium (RTSS 2007), pp. 379-388, Tucson, Arizona, Dec. 3-6, 2007.
 - [HCMDSS07a] Mu Sun, Qixin Wang, and Lui Sha, "Building Safe and Reliable MD PnP Systems", in Joint Workshop on High Confidence Medical Devices, Software, and Systems (HCMDSS) and Medical Devices Plug-and-Play (MD PnP), June, 2007.
 - [HCMDSS07b] Jennifer C. Hou, Qixin Wang, et. al., "PAS: A Wireless-enabled, Sensor-integrated Personal Assistance System for Independent and Assisted Living", in Joint Workshop on High Confidence Medical Devices, Software, and Systems (HCMDSS) and Medical Devices Plug-and-Play (MD PnP), June, 2007.
 - [ICSMC08] Qixin Wang, Wook Shin, Xue Liu, et. al., "i-Living: An Open System Architecture for Assisted Living", (invited paper) in Proc. of IEEE International Conference on Systems, Man, and Cybernetics 2008.
 - [RTSS05] Qixin Wang, Xue Liu, Weiqun Chen, Wenbo He, and Marco Caccamo, "Building Robust Wireless LAN for Industrial Control with DSSS-CDMA Cellphone Network Paradigm", in Proc. of the 26th IEEE International Real-Time Systems Symposium (RTSS 2005), Miami, USA, December, 2005. (Power Point/Poster/Short Power Point)
 - [ICAS05] Xue Liu, Rong Zheng, Jin Heo, Qixin Wang, and Lui Sha, "Timing Control for Web Server Systems Using Internal State Information", in Proc. of Joint International Conference on Autonomic and Autonomous Systems and International Conference on Networking and Services (ICAS-ICNS 2005), 2005.
 - [RTSS04] Qixin Wang, Rong Zheng, Ajay Tirumala, Xue Liu, and Lui Sha, "Lightning: A Fast and Lightweight Acoustic Localization Protocol Using Low-End Wireless Micro-Sensors", in Proc. of the 25th IEEE International Real-Time Systems Symposium (RTSS 2004), Lisbon, Portugal, December, 2004. (Power Point)
 - [RTSS03] Xue Liu, Qixin Wang, Lui Sha and Wenbo He, "Optimal QoS Sampling Frequency Assignment for Real-Time Wireless Sensor Networks", in Proc. of the 24th IEEE International Real-Time Systems Symposium (RTSS 2003), Cancun, Mexico, December, 2003.
 - [IPSN03] Qixin Wang, Wei-Peng Chen, Rong Zheng, Kihwal Lee, and Lui Sha, "Acoustic Target Tracking Using Tiny Wireless Sensor Services", in Proc. of the 2nd International Workshop on Information Processing in Sensor Networks (IPSN03), Lecture Notes in Computer Science 2634, Springer, 2003.